

This listing of claims replaces all prior versions, and listings of claims in the instant application:

**Listing of Claims:**

1. (Presently Amended) A data structure comprising:  
a combined thread control block/message structure, said combined thread control block/message structure comprising:  
a thread control block structure, wherein said thread control block is described by a first data structure; said thread control block structure and including a message structure embodied therein, wherein said message is described by a second data structure and said first data structure comprises said second data structure said thread control block structure configured to store information used to control execution of a thread, said message structure configured to store a message.
2. (Cancelled) Please Cancel Claim 2, without prejudice.
3. (Cancelled) Please Cancel Claim 2, without prejudice.
4. (Presently Amended) The data structure of claim 1, wherein said thread control block structure ~~first data structure~~ further comprises:  
a process control block pointer, wherein said process control block pointer points to a process control block;  
processor information; and  
stack information.

5. (Original) The data structure of claim 4, wherein said process control block comprises:

memory information;  
thread information;  
device driver information; and  
stack information.

6. (Original) The data structure of claim 4, wherein said processor information comprises:

a processor identifier; and  
thread information.

7. (Presently Amended) The data structure of claim 1, wherein said message ~~second data~~ structure further comprises:  
control information.

8. (Presently Amended) The data structure of claim 7, wherein said message ~~second data~~ structure further comprises:  
data.